

High-Power and High-Efficiency AlGaIn/GaN HEMT Operated at 50 V Drain Bias Voltage

T.Kikkawa¹, M.Nagahara², N.Adachi², S.Yokokawa², S. Kato², M.Yokoyama², M.Kanamura¹,
Y.Yamaguchi², N.Hara¹, and K.Joshin¹

¹Fujitsu Laboratories Ltd., 10-1 Morinosato-Wakamiya, Atsugi, Kanagawa, 243-0197, Japan

²Fujitsu Quantum Devices, 1000 Kamisukiawara, Nakakoma-Gun, Yamanashi, 409-3883, Japan

Abstract — We describe high power 70 W CW operation at 52 V drain bias voltage (V_{ds}) using 24-mm gate-periphery AlGaIn/GaN HEMTs on SiC substrate. A 48 W output power with the drain efficiency of 60% was also obtained at V_{ds} of 50 V under the efficiency-matched condition near class-B. At 40 V, the drain efficiency reached 68%. V_{ds} dependence of third order-intermodulation (IM3) was also characterized at V_{ds} up to 50 V. This is the first report about IM3 profile characterization for a large gate-periphery device at V_{ds} of 50 V. We also investigated RF-stress life test at V_{ds} up to 40 V. The AlGaIn/GaN HEMT in this study exhibited good reliability over 100 h.

I. INTRODUCTION

AlGaIn/GaN-based high electron mobility transistors (HEMTs) are promising for microwave power applications, including wireless base stations. There are many reports related to high output power characteristics [1-4]. However, a few papers exhibited high efficiency characteristics using large gate-periphery device over 40 V drain bias voltage (V_{ds}) [5]. Base station system demands high efficiency from amplifier. So we need to make the quiescent bias current low. In this paper, we demonstrate high power performance near class-B operation. A 70 W output power for a 24-mm gate periphery HEMT was obtained at V_{ds} of 52 V. We also report 48 W output power with power added efficiency (PAE) of 48% and drain efficiency (nd) of 60% at V_{ds} of 50 V under efficiency-matched conditions.

In addition, a low distortion characteristic near class-B operation is required for the base stations using digitally modulation scheme such as IMT-2000. The crest factor of a peak to average power ratio is as high as 8dB. The distortion at the averaged power level, that is 8dB back off output level from saturation, is important. Therefore, in addition to the high saturation power (P_{sat}), high efficiency and low distortion at 8-10 dB backed-off power level from P_{sat} is required. Moreover, predistortion system prefers the simple IMD profile without a sweet

spot because it easily predicts the predistorting input signal.

We previously reported that superior linearity profile can be obtained at V_{ds} of 30 V for a 1mm-gate periphery AlGaIn/GaN HEMT [6]. In this paper, we demonstrate the superior IMD profile can also be obtained at high V_{ds} for a large-gate periphery device. And furthermore, we investigated the V_{ds} dependence of linearity characteristics up to 50V drain bias operation. This is the first report related to IM3 profile up to 50 V drain bias operation for a large periphery HEMT.

Reliability has become an important issue to be discussed for manufacturing of AlGaIn/GaN-HEMTs. We performed the RF-stress examination to investigate stability of HEMT performance under continuous power

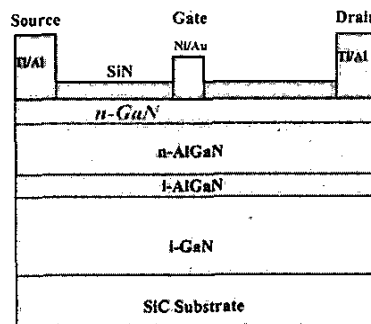


Fig. 1. Schematic drawing of investigated surface-charge-controlled n-GaN-cap structures. Thin n-type GaN cap layer was grown on AlGaIn/GaN structure. SiN passivation was formed on GaN cap layer between electrodes.

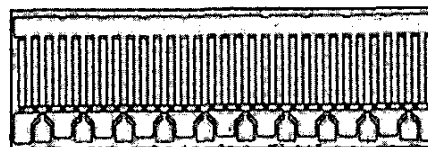


Fig. 2. A 24-mm gate periphery AlGaIn/GaN HEMT.

operation. A CW P3dB RF-power measurement at V_{ds} of 40 V exhibited good reliability over 100 h.

II. EXPERIMENTAL

To obtain higher RF-power and efficiency, we have to suppress frequency dependent characteristics, such as large transconductance (g_m) dispersion, gate-lag and current [7-9]. We controlled the polarization-induced surface charge by n-type doping in a thin GaN cap on AlGaN and stabilized n-GaN-surface between electrodes using SiN [10].

Figure 1 shows the device structures on SiC substrates investigated in this study. Detailed fabrication method was described in the previous papers [4,6,10]. Gate width (W_g) was varied from 40 μm to 24mm (Fig.2). Power performance was measured using load-pull system. Quiescent drain current (I_{dsq}) is 1.4% I_{fmax} near class B, which is mainly used in base station system [6].

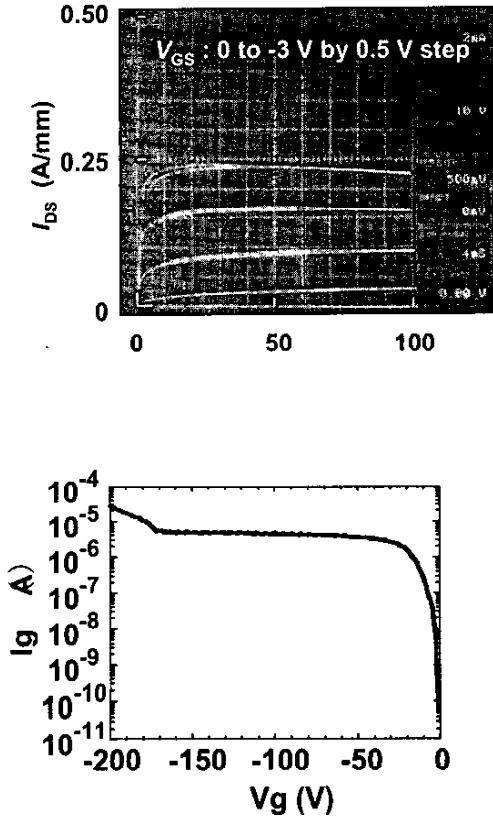


Fig. 4. Gate-drain inverse I-V characteristics of 80 μm gate periphery.

III. RESULTS AND DISCUSSION

A. High Power Performance

Figure 3 shows typical I-V characteristics in this study. We realized high on-state breakdown voltage over 100 V and good pinched-off till 100 V. [4,10]. Fig. 4 shows gate-drain inverse I-V characteristics under source is open. Breakdown voltage defined as a gate-drain voltage at a drain current of $-0.5 \mu\text{A/mm}$ is over 200 V.

Fig. 5 shows power performance measured at V_{ds} of 52 V under power-matched conditions when I_{dsq} was 1.4% I_{fmax} . 70 W CW saturation power was achieved for a 24-mm periphery device. Figure 6 shows efficiency-matched condition case at V_{ds} of 50V. 48 W CW-output power could be obtained with high drain efficiency of 60% and high PAE of 48%, respectively. Figure 7 shows the V_{ds} dependence of P_{sat} , Gain, drain efficiency, and PAE under efficiency matched conditions in Fig.6. At V_{ds} of 40 V, we achieved high drain efficiency of 68% and high PAE of 50%. This is the state of the art high efficiency over 40V for a large periphery device with over 36 W output power.

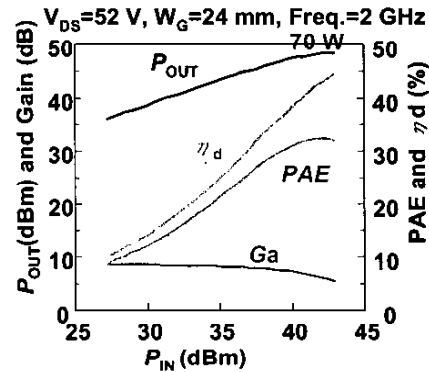


Fig. 5. CW power measurement results at V_{ds} of 52 V.. Measured frequency is 2.2 GHz. Gate width is 24mm. Packaged chip was measured. Power-matched-condition.

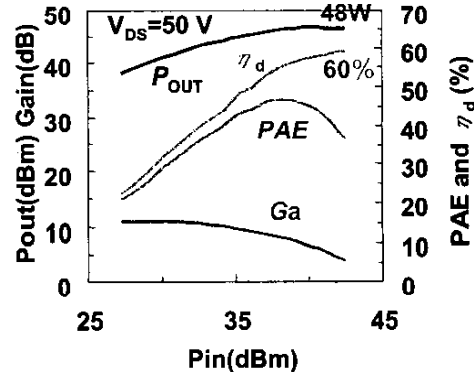


Fig. 6. CW power measurement results at V_{ds} of 50 V.. Measured frequency is 2.2 GHz. Efficiency-matched-condition.

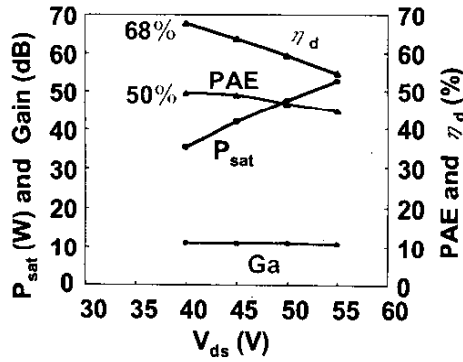


Fig. 7. CW power measurement results, as a function of V_{ds} . Measured frequency is 2.2 GHz. Gate width is 24 mm. Efficiency-matched-condition.

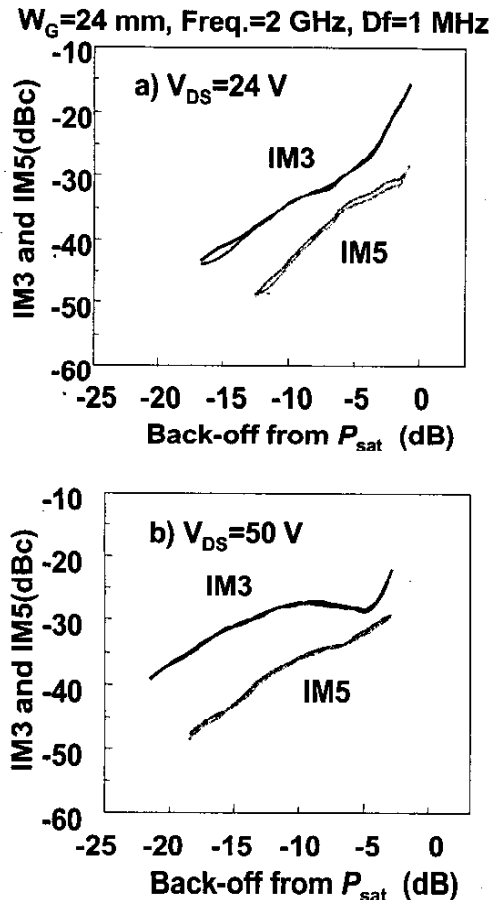


Fig. 8 2-tone intermodulation characteristics measured at a) 24 V and b) 50 V. Measured frequency is 2.2 GHz. I_{dsq} is 1.4% I_{fmax} near class-B. Gate width is 24 mm.

B. Intermodulation Characteristics

Figure 8 shows intermodulation characteristics measured at 24 V or 50 V for a 24-mm gate periphery HEMT. As we previously reported using a small gate periphery device [6], simple IM3 profile could also be obtained for large gate-periphery device at V_{ds} of 24 V. However, at 50 V, IM3 profile had sweet spot feature. In this case, same matching circuit was used for various V_{ds} operation between 12 and 50 V. Figure 9 shows V_{ds} dependence of IM3, 2-tone drain efficiency and PAE at 8 dB backed-off power with 1-tone power parameters. 1-tone PAE-max over 50% was obtained around 20-30 V. Thus, we suppose that intermodulation-matched condition was also realized around V_{ds} of 24 V. The 2-tone drain efficiency and PAE at 8 dB backed-off power lever from P_{sat} showed small difference within 5 % among various V_{ds} operations. This indicates that only adjusting matching circuit might result in more superior IM3 profile at 50 V.

C. Reliability

We investigated preliminary reliability performance, which becomes a most important issue at the high drain bias operation. The RF-stress test was examined under P3dB conditions at V_{ds} of 30 V and 40 V. To accelerate the aging test, I_{dsq} was settled at 7.5% I_{fmax} which is larger than power measurement in Figs.5-10. G_m profiles are the same between before and after RF-stress test operated at 30 V as shown in Fig.10. Power characteristics measured at 40 V is also shown in Fig.11. After 162-hour

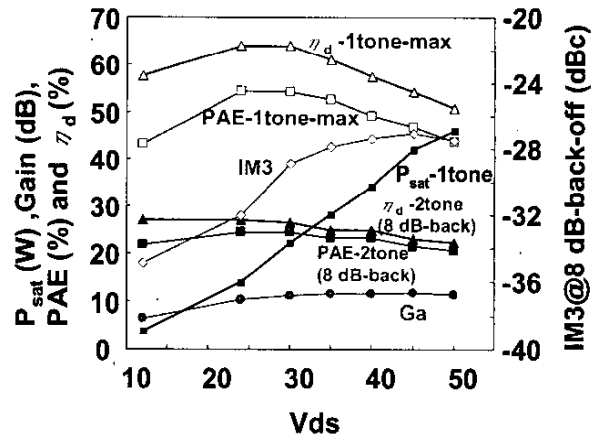


Fig.9. 2-tone intermodulation characteristics as a function of V_{ds} . Matching circuit was not changed in this measurement. 2-tone IM3, drain efficiency and PAE at 8 dB backed-off power lever from P_{sat} are shown. 1-tone P_{out} and Gain are also shown with 1-tone maximum drain efficiency and maximum PAE. I_{dsq} is 1.4% I_{fmax} near class-B. Gate width is 24 mm.

RF-stress test, Pout and Gain decreased only 0.2 dBm, Although some reports showed degradation phenomena at 30 V operations, we never detected significant degradation over 100 h at 40 V operations, indicating that the AlGaIn/GaN HEMTs in our study has long reliability at 40 V. This is the first report of stable operation at 40 V

IV. CONCLUSION

In summary, we fabricated 24mm-gate-periphery AlGaIn/GaN HEMTs using a surface-charge-controlled structure on SiC. 70 W CW saturation power was obtained at Vds of 52 V. 48 W CW output power with high drain efficiency of 60 % was also obtained at Vds of 50 V. IM3 of 24-mm device at Vds of 24 V indicated simple good profile. IM3 at 50 V has small sweet spot but it will be improved by adjusting matching circuit. RF-stress test under P3dB showed stable performance up to 40 V. These results verify that the high-efficiency AlGaIn/GaN in this study has suitable performance for base station amplifier.

REFERENCES

- [1] Y-F Wu, D.Kpoplnck, J.P.Ibbetson, P.Parikh, B.P. Keller, and U.K.Mishra, "Very-High Power Density AlGaIn/GaN HEMTs," *IEEE Trans. Electron. Devices*, Vol.48, pp586-590, March 2000.
- [2] Y.Ando, Y.Okamoto, H.Miyamoto, N.Hayama, T.Nakayama, K.kasahara, and M.Kuzuhara, "A 110-W AlGaIn/GaN Heterojunction FET on Thinned Sapphire Substrates," *2001 IEDM Tech. Digest*, pp381-384, December 2001.
- [3] J.W.Palmour, S.T.Sheppard, R.P.Smith, S.T.Allen, W.L.Pribble, T.J.Smith, Z.Ring, J.J.Sumakeris, A.W.Saxler, and J.W.Miligan, "Wide Bandgap Semiconductor Devices and MMICs for RF Power Applications," *2001 IEDM Tech. Digest*, pp385-388, December 2001.
- [4] T.Kikkawa, M.Nagahara, T.Kimura, S.Yokokawa, S.Kato, M.Yokoyama, Y.Tateno, K.Horino, K.Domen, Y.Yamaguchi, N.Hara, and K.Joshin, "A 36 W CW AlGaIn/GaN-Power HEMT Using Surface-Charge-Controlled Structure," *2002 MTT-s Digest*, pp1815-1818, June 2002.
- [5] W.L.Pribble, J.W.Palmour, S.T.Sheppard, R.P.Smith, S.T.Alén, T.J.Smith, Z.Ring, J.J.Sumakeris, A.W.Saxler, and J.W.Milligan, "Application of SiC MESFET and GaN HEMTs in Power Amplifier Design," *2002 MTT-S Digest*, pp1819-1822, June 2002
- [6] M.Nagahara, T.Kikkawa, N.Adachi, Y.Tateno, S.Kato, M.Yokoyama, S.Yokogawa, T.Kimura, Y.Yamaguchi, N.Hara, and K.Joshin, "Improved Intermodulation Distortion Profile of AlGaIn/GaN HEMT at High Drain Bias Voltage," *2002 IEDM Tech. Digest*, December 2002.
- [7] I.Daumiller, D.Theron, C.Caquiere, A.Vescan, R.Dietrich, A.Wieszt, H.Leier, "Current Instability in GaN-Based Devices," *IEEE Trans. Electron. Devices* vol.22, pp 62-64, Feb. 2001.
- [8] S.C.Binari, K.Ikossi, J.A.Roussos, W.Kruppa, D.Park, H.B.Dietrich, D.D.Koleske, A.E.Wickenden, and R.L.Henry, "Trapping Effects and Microwave Power Performance in AlGaIn/GaN HEMTs," *IEEE Trans. Electron. Devices*, Vol.48, pp465-471, March 2001.
- [9] R.Vetury, N.Q.Zhang, S.Keller, and U.K.Mishra, "The Impact of Surface States on the DC and RF Characterization of AlGaIn/GaN HFETs," *IEEE Trans. Electron. Devices*, Vol.48, pp560-566, March 2001.
- [10] T.Kikkawa, N.Nagahara, N.Okamoto, Y.Tateno, Y.Yamaguchi, N.Hara, K.Joshin, and P.M.Asbeck, "Surface-Charge-Controlled AlGaIn/GaN-Power HFET without Current Collapse and Gm Dispersion," *2001 IEDM Tech. Digest*, pp585-588, December 2001.

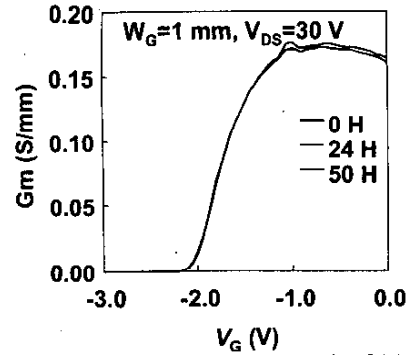


Fig. 10. Gm profile before and after P3dB RF-stress test at 30 V. During RF stress test, Idsq is 7.5% Ifmax.

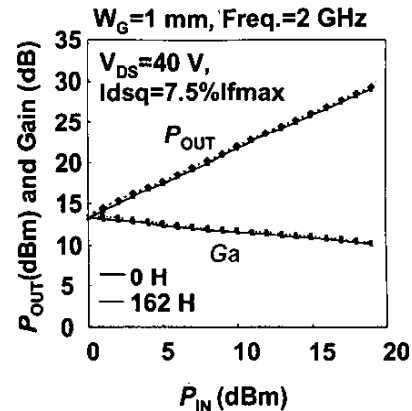


Fig. 11. Power characteristics before and after P3dB RF-stress test at 40 V. Idsq is 7.5% Ifmax. Gain-matched conditions. Pout difference is 0.2 dBm after 162 h.